

**The VLSI Implementation Of A GaAs GIC Switched  
Capacitor Filter**

**By Harry G. Oldland**

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Oldland, Harry G. URI: Presented is the initial step for the eventual implementation of a programmable GIC switched capacitor filter in a GaAs VLSI

<https://calhoun.nps.edu/handle/10945/9152>

This paper presents a VLSI architecture to implement the forward and inverse two dimensional Discrete Wavelet Transform (DWT), to compress medical images for st

<http://www.sciencedirect.com/science/article/pii/S1077201499901717>

Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining thousands of transistors into a single chip.

[http://en.wikipedia.org/wiki/Very-large-scale\\_integration](http://en.wikipedia.org/wiki/Very-large-scale_integration)

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Currently, fuzzy controllers are the most popular choice for hardware implementation of  
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ABSTRACT VLSI Implementation of Digital Circuits by OPINDER SHARMA May  
2011 Advisor: Dr. Harpreet Singh Major: Computer Engineering Degree: Master of  
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[http://digitalcommons.wayne.edu/oa\\_theses/97/](http://digitalcommons.wayne.edu/oa_theses/97/)

Analog VLSI Implementation of Neural Systems. Editors: Mead, Carver, Ismail,  
Mohammed (Eds.)  
<http://www.springer.com/us/book/9780792390404>

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implementation of Karatsuba algorithm for multi-digit multiplication was investigated.  
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Engineering Students.  
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In this Paper, Digital Modulators is designed and Implemented using XILINX ISE Tool based on Very Large Scale Integration (VLSI). This is majorly to implement and  
<http://www.ijrcct.org/index.php/ojs/article/view/354>

dc.contributor.author: Cavallaro, Joseph R. Keleher, Michael P. Price, Russell H. Thomas, Gregory S. dc.creator: Cavallaro, Joseph R. Keleher, Michael P.

<http://scholarship.rice.edu/handle/1911/19757?show=full>

VLSI implementation of OFDM modem Aseem Pandey, Shyam Ratan Agrawalla & Shrikant Manivannan Wipro Technologies. OFDM is a multi-carrier system where data bits

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